JAN 1 8 2005 PARADEMARKS

Blakely, Sokoloff, Laylor & Zatman LLP (714) 557-3500 Title: SYSTEM AND METHOD FOR VERIFYING THE INTEGRITY OF STORED INFORMATION WITHIN AN ELECTRONIC DEVICE

1st Named Inventor: Robert P. Hale

Application No.: 09/675,113

Docket No.: 42390P9144

Sheet: 1 of 7

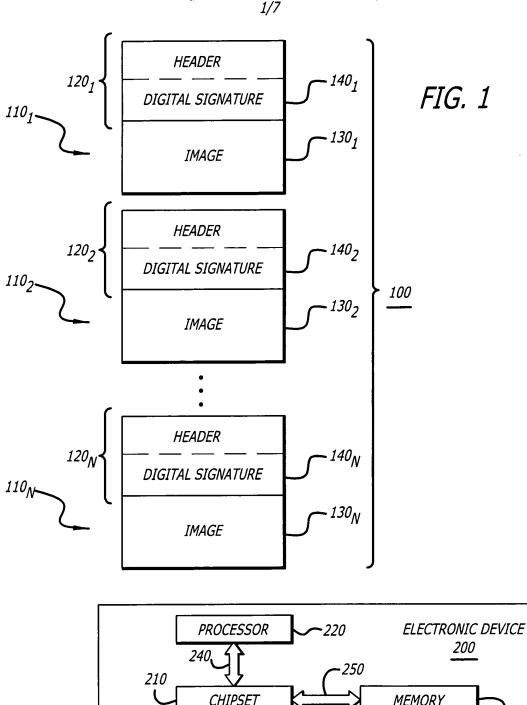


FIG. 2

PROCESSOR 220

ELECTRONIC DEVICE
200

CHIPSET MEMORY
230

COMMUNICATION DEVICE
MEMORY
290

NON-VOLATILE
MEMORY
290

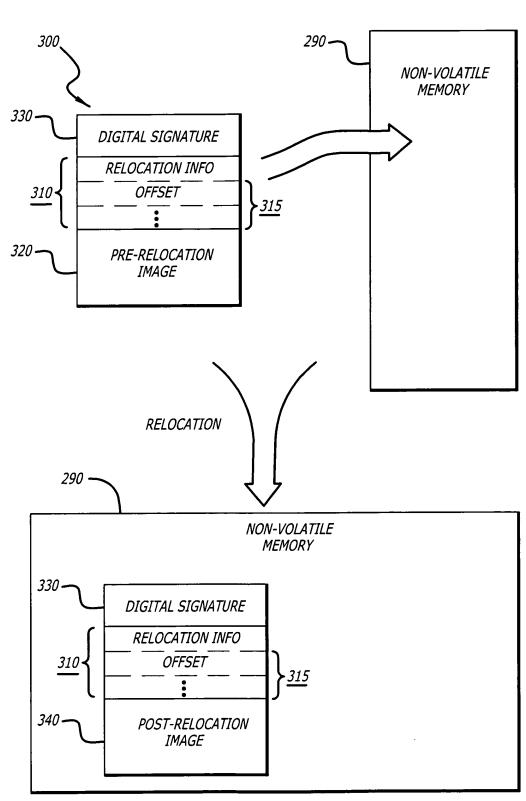
NON-VOLATILE
MEMORY

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FIG. 3



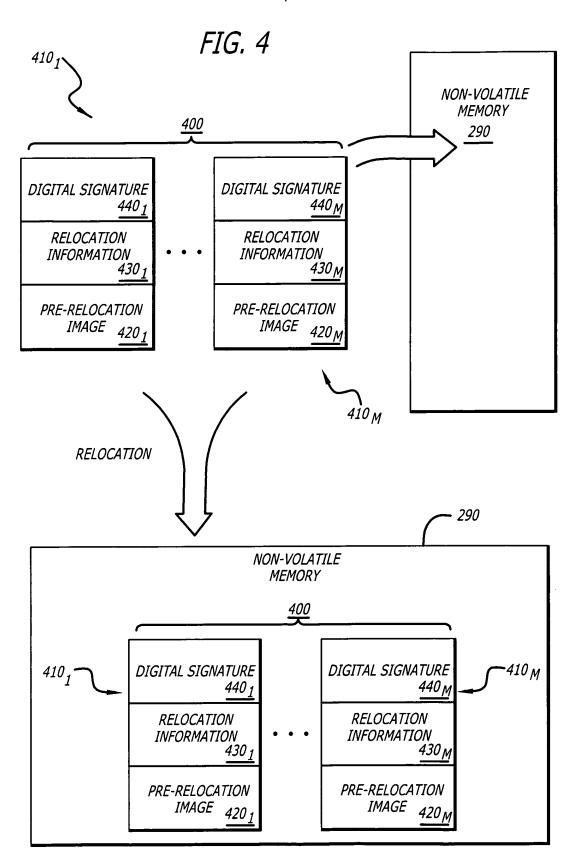
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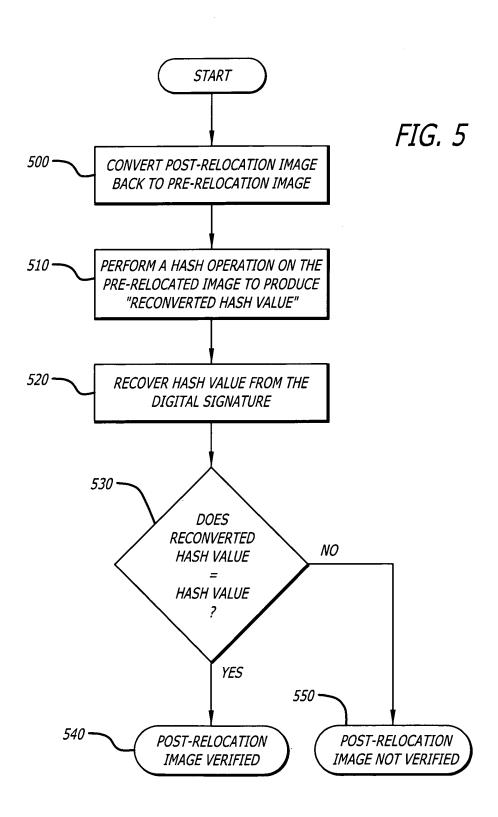


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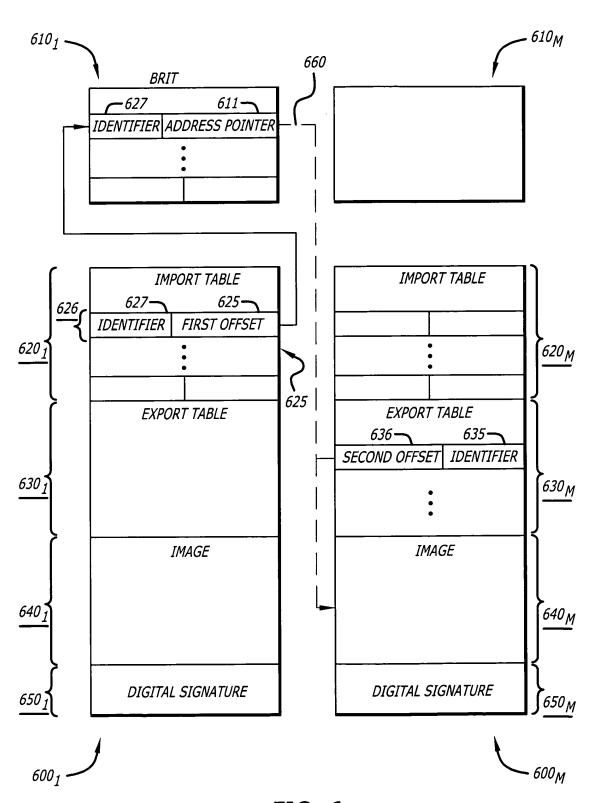


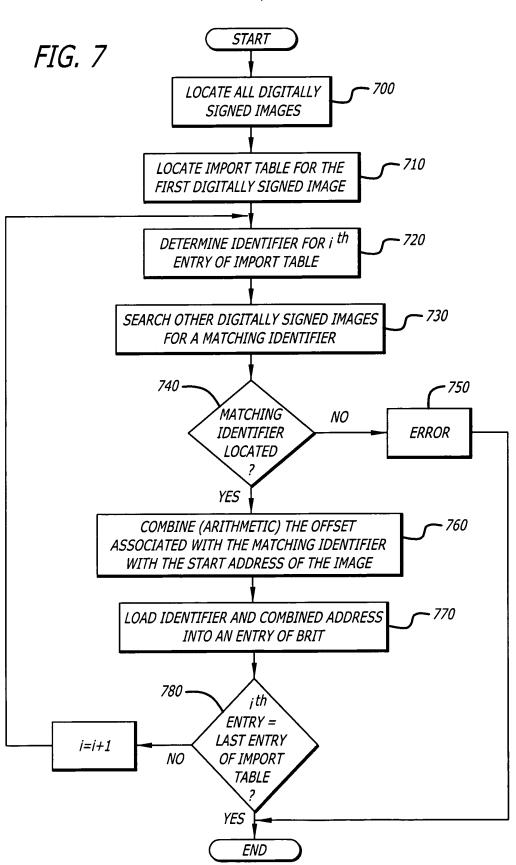
FIG. 6

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